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| 47394 | 7590 | 11/04/2009 | EXAMINER | |
| HITT GAINES, PC | | | MATTIS, JASON E | |
| ALCATEL-LUCENT | | | | |
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

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docket@hittgaines.com

DETAILED ACTION

1. This Advisory Action is in response to the Amendment After Final filed 10/13/09. Claims 1-20 are currently pending in the application.

Response to Arguments

2. Applicant's arguments filed 10/13/09 have been fully considered but they are not persuasive.

Applicant argues that:

“Reches does not teach crossbar FIFOs interposed between a respective destination FIFO and each input port. On the contrary, Fig. 1 of Reches teach that quest $Q(M,1)$ through $Q(M,N)$ in each input port are interposed only to one input. As such, the cited portion of Reches does not teach or suggest each of n outputs having n crossbar FIFOs interposing corresponding ones of n inputs and a destination FIFO”.

The Examiner respectfully disagrees with this assessment of the teachings of Reches. As pointed out by the Applicant's arguments, Reches discloses output ports having an output queue, which corresponds to the claimed destination FIFO (See page 4 paragraph 52 of Reches). Reches also discloses that each input port maintains output queues, corresponding to the claimed crossbar FIFOs, for each possible output port (See page 4 paragraph 55 and Figure 1 of Reches). Thus, for example, input port M

Art Unit: 2461

has N output queues $Q(M,1)$ - $Q(M,N)$ that uniquely correspond to output ports 1-N respectively. Conversely, each output port of Reches also has N dedicated output ports, with one from each of the N input ports. Each output port, i.e. output port M, has N dedicated output queues, i.e. output queues $Q(1,M)$ - $Q(N,M)$, with each of the N dedicated output queues interposing a different one of the N inputs. Thus, for example, output port 1 has a queue $Q(1,1)$ interposed between output port 1 and input 1, a queue $Q(2,1)$ interposed between output port 1 and input 2, a queue $Q(3,1)$ interposed between output port 1 and input 3, a queue $Q(M,1)$ interposed between output port 1 and input M, etc. Therefore, Reches discloses n outputs (See Figure 1 of Reches for reference to the outputs of output ports 1-N), each of said outputs having a destination first-in, first-out buffer (See page 4 paragraph 52 of Reches for reference to each output port having at least one output queue) and n crossbar FIFOs interposing corresponding ones of said n inputs and said destination FIFO (See page 4 paragraph 55 of Reches for reference to each output having one output queue for each of input ports 1-N that interposes the output queues of the output ports and different corresponding inputs of the input ports), as claimed.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JASON E. MATTIS whose telephone number is (571)272-3154. The examiner can normally be reached on M-F 8AM-5:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Huy Vu can be reached on (571)272-3155. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Jason E Mattis
Primary Examiner
Art Unit 2461

JEM

/Jason E Mattis/
Primary Examiner, Art Unit 2461